## **MORNSUN®**

150W isolated DC-DC converter Ultra-wide input and regulated single output











EN62368-1







#### **FEATURES**

- Ultra-wide 4:1 input voltage range
- High efficiency up to 91%
- I/O isolation test voltage 2.25k VDC
- Operating ambient temperature range: -40°C to +85°C
- Input under-voltage protection, output short-circuit, over-current, over-voltage, over-temperature protection
- Five-sided metal shielded package
- Industry standard ¼-Brick package and pin-out

URF48\_QB-150W(F/H)R3(A5/A6) series are isolated 150W DC-DC products with 4:1 input voltage. They feature efficiency up to 91%, 2250VDC input to output isolation, operating ambient temperature of -40°C to +85°C, input under-voltage, output short circuit, over-current, over-voltage, over-temperature protection. The products meet CLASS A of CISPR32/EN55032 EMI standards by adding the recommended external components and they are widely used in applications such as battery powered systems, industrial controls, electricity, instrumentation, railway, communication and intelligent robotic.

Selection Guide											
Certification Part No.®	CT-I	Input Volta	Input Voltage (VDC)		Output		Max.				
	Dart No ®	Ctrl Logic <sup>2</sup>	Nominal (Range)	Max. <sup>®</sup>	Voltage (VDC)	Current (A) (Max.)	Efficiency(%) Min./Typ.	Capacitive Load(µF)			
	URF4805QB-150W(F/H)R3	Р	P		5	30	86/88	6000			
	URF4812QB-150W(F/H)R3	Р	48 (18-75)		12	12.5	89/91	2000			
	URF4815QB-150W(F/H)R3	Р			15	10	87/89	2000			
	URF4824QB-150W(F/H)R3	Р		48	48			24	6.25	89/91	1000
FAI	URF4848QB-150W(F/H)R3	Р					48	3.13	89/91	450	
EN	URF4805QB-150W(H)R3A5/A6	Р		80	5	30	84/86	6000			
	URF4812QB-150W(H)R3A5/A6	Р	-		12	12.5	87/89	2000			
	URF4815QB-150W(H)R3A5/A6	Р			15	10	85/87	2000			
	URF4824QB-150W(H)R3A5/A6	Р			24	6.25	87/89	1000			
	URF4848QB-150W(H)R3A5/A6	Р	P		48	3.13	87/89	450			

①Use Fr suffix is for added aluminum baseplate and "H" suffix for heat sink mounting, Use "A5" suffix for chassis mounting and "A6" suffix for DIN-Rail mounting. We recommend to choose modules with a heat sink for enhanced heat dissipation and applications with extreme temperature requirements;

- ②"P" means positive logic, "N" means negative logic;
- 3 Exceeding the maximum input voltage may cause permanent damage;
- (4) The minimum input voltage range and the start-up voltage of the A5/A6 product model are 1VDC higher than the horizontal package.

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Input Current (full load/no-load)	Nominal input voltage		3512/100	3634/200	A
Reflected Ripple Current	Nominal input voltage		100		mA

**MORNSUN®** 

MORNSUN Guangzhou Science & Technology Co., Ltd.

# DC/DC Converter URF48\_QB-150W(F/H)R3(A5/A6) Series



Surge Voltage (1sec. max.)		-0.7	-	90	
Start-up Voltage			-	18	VDC
Input Under-voltage Protection		14	16		
Input Filter			Pi filte	∍r	
Hot Plug		Unavailable			
	Module on	Ctrl pin o	Ctrl pin open or pulled high (3.5-12VDC)		
Ctrl <sup>®</sup>	Module off	Ctrl pin pulled low to GND (0-1		GND (0-1.2	VDC)
	Input current when off		2	10	mA
Note: ①The Ctrl pin voltage is reference	ed to input GND.	'			

Operating Conditions		Min.	Тур.	Max.	Unit
0%-100% load			±1	±3	
Input voltage variation fro	m low to high at full load		±0.2	±0.5	%
5%-100% load			±0.5	±0.75	
25% load step change			300	500	μs
OFW Is and store above as	5V output		±3	±7.5	%
25% load step change	others		±3	±5	76
Full load			-	±0.03	%/℃
20MHz bandwidth	20MHz bandwidth		150	250	mVp-p
		90	-	110	00.7
			-	105	%Vo
Max. Case Temperature	Max. Case Temperature		115	120	°C
Input voltage range		110	130	160	%Vo
		110	130	150	%lo
			Hiccup, continuous, self-recovery		
	Operating Conditions  0%-100% load  Input voltage variation fro  5%-100% load  25% load step change  25% load step change  Full load  20MHz bandwidth  Max. Case Temperature	Operating Conditions  0%-100% load  Input voltage variation from low to high at full load  5%-100% load  25% load step change  25% load step change  Full load  20MHz bandwidth  Max. Case Temperature	Operating Conditions         Min.           0%-100% load            Input voltage variation from low to high at full load            5%-100% load            25% load step change            25% load step change         5V output            Full load            20MHz bandwidth            Max. Case Temperature            Input voltage range         110	Operating Conditions         Min.         Typ.           0%-100% load          ±1           Input voltage variation from low to high at full load          ±0.2           5%-100% load          ±0.5           25% load step change          300           25% load step change         5V output          ±3           Full load              20MHz bandwidth              Max. Case Temperature          115           Input voltage range         110         130	Operating Conditions         Min.         Typ.         Max.           0%-100% load          ±1         ±3           Input voltage variation from low to high at full load          ±0.2         ±0.5           5%-100% load          ±0.5         ±0.75           25% load step change          300         500           25% load step change          ±3         ±7.5           others          ±3         ±5           Full load          -         ±0.03           20MHz bandwidth          150         250           90          110           Max. Case Temperature          115         120           Input voltage range         110         130         160

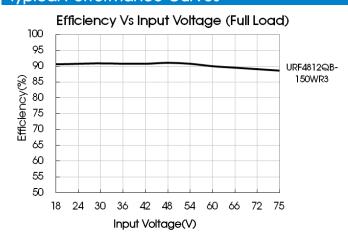
General Specification	ons					
Item	Operating Conditions		Min.	Тур.	Max.	Unit
	Electric Strength Test for 1	Input-output	2250	-		
Isolation	minute with a leakage	Input-case	1500	-		VDC
	current of 5mA max	Output-case	500	-	-	
Insulation Resistance	Input-output resistance at 50	00VDC	100	-	_	ΜΩ
Isolation Capacitance	Input-output capacitance o	nt 100KHz/0.1V	-	2200	_	pF
Operating Temperature				_	+85	- °C
Storage Temperature			-55	_	+125	
Storage Humidity	Non-condensing	Non-condensing		_	95	%RH
Pin Soldering Resistance	Wave-soldering, 10 seconds	Wave-soldering, 10 seconds		_	260	- °C
Temperature	Soldering spot is 1.5mm awa	Soldering spot is 1.5mm away from case for 10 seconds		_	300	
		URF48xxQB-150WR3	-	_	7.5	°C/W
Thermal Resistance	Free air convection (20LFM)	URF48xxQB-150WFR3	-	_	6.3	
	(ZOLI IVI)	URF48xxQB-150WHR3	-	_	5.2	
Shock And Vibration				161373 - Cate	gory 1, Gro	ade B
Switching Frequency	PWM mode	PWM mode		250	_	KHz
MTBF	MIL-HDBK-217F@25°C		500		-	K hours

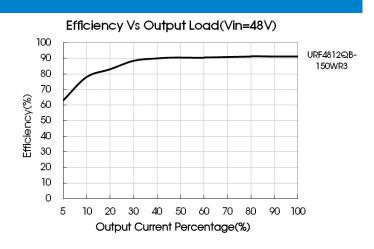


Mechanical Spec	cifications			
Case Material	Aluminum alloy case, black plastic botto	m, flame-retardant and heat-resistant (UL94 V-0)		
	URF48xxQB-150WR3	61.8 x 40.2 x 12.7 mm		
	URF48xxQB-150WFR3	62.0 x 56.0 x 14.6 mm		
	URF48xxQB-150WHR3	61.8 x 40.2 x 27.7 mm		
Dimension	URF48xxQB-150WR3A5	135.00 x 70.00 x 22.6mm		
	URF48xxQB-150WR3A6	137.00 x 70.00 x 28.10mm		
	URF48xxQB-150WHR3A5	135.00 x 70.00 x 36.20mm		
	URF48xxQB-150WHR3A6	137.00 x 70.00 x 37.20mm		
	URF48xxQB-150WR3	89.0g(Typ.)		
	URF48xxQB-150WFR3	109.0g(Typ.)		
	URF48xxQB-150WHR3	120.0g(Typ.)		
Weight	URF48xxQB-150WR3A5	165.0g(Typ.)		
	URF48xxQB-150WR3A6	235.0g (Typ.)		
	URF48xxQB-150WHR3A5	196.0g(Typ.)		
	URF48xxQB-150WHR3A6	266.0g (Typ.)		
Cooling Method	Free air convection (20LFM)	Free air convection (20LFM)		

Electromagnetic Compatibility (EMC)				
Emissions CE RE	CISPR32/EN55032	CLASS A (see Fig. 2 for recommended circuit)		
	CISPR32/EN55032	CLASS A (see Fig. 2 for recommended circuit)		
	ESD	IEC/EN61000-4-2, EN50121-3-2	Contact ±6KV Air ±8KV	perf.Criteria B
RS	IEC/EN61000-4-3, EN50121-3-2	10V/m	perf.Criteria A	
	EFT	IEC/EN61000-4-4, EN50121-3-2	±2KV(see Fig. 2 for recommended circuit)	perf.Criteria A
Immunity S	Surge	EN50121-3-2	differential mode $\pm 1$ KV, 1.2/50us, source impedance $42\Omega$ (see Fig.2 for recommended circuit)	perf.Criteria B
	CS	IEC/EN61000-4-6, EN50121-3-2	10 Vr.m.s	perf.Criteria A

#### Typical Performance Curves

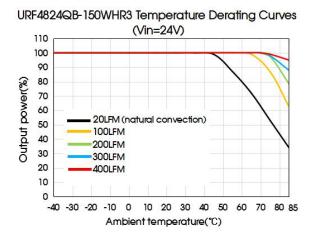






URF4824QB-150WFR3 Temperature Derating Curves (VIn=24V) 110 100 90 80 Output power(%) 70 60 50 20LFM (natural convection) 40 1001 FM 200LFM 30 300LFM 20 400LFM 10 -40 -30 -20 -10 0 10 20 30 40 50 70 80 85 60

Ambient temperature(°C)

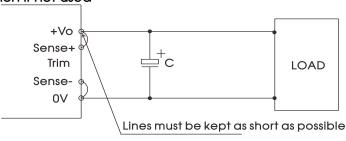


#### Notes:

1) Product application thermal design should be referred to the recommended PCB layout and recommended heat dissipation structure, please see DC-DC Converter Application Notes for specific operation.

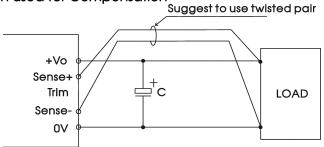
#### Remote Sense Application

1. Remote Sense Connection if not used



#### Notes:

- (1) If the sense function is not used for remote regulation the user must connect the +Sense to +Vo and -Sense to 0V at the DC-DC converter pins and will compensate for voltage drop across pins only.
- (2) The connections between Sense lines and their respective power lines must be kept as short as possible, otherwise they may be picking up noise, interference and/or causing unstable operation of the power module.
- 2. Remote Sense Connection used for Compensation



#### Notes:

- (1) Using remote sense with long wires may cause unstable output, please contact technical support if long wires must be used.
- (2) PCB-tracks or cables/wires for Remote Sense must be kept as short as possible. Twisted pair or shielded wires are suggested for remote compensation and must be kept as short as possible.
- (3) We recommend using adequate cross section for PCB-track layout and/or cables to connect the power supply module to the load in order to keep the voltage drop below 0.3V and to make sure the power supply's output voltage remains within the specified range.
- (4) Note that large wire impedance may cause oscillation of the output voltage and/or increased ripple. Consult technical support or factory for further advice of sense operation.

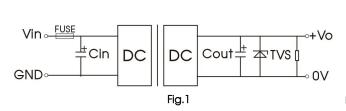


#### Design Reference

#### 1. Typical application

(1) We recommended using the recommended circuit shown in Fig.1 during product testing and application, otherwise please ensure that at least a 220µF electrolytic capacitors is connected at the input in order to ensure adequate voltage surge suppression and protection.

- (2) We recommended increasing the value of Cin and pay attention to the unstable input voltage if the product input side is paralleled with motor drive circuit and/or larger energy transient circuits, to ensure the stability of input terminal and avoid repeatedly start-up problems due to input voltage lower than under-voltage protection point.
- (3) We recommended increasing the output capacitance with limited to the capacitive load specification and/or increasing the voltage clamping circuit(such as TVS) if the output terminal is inductive device such as relay or a motor, to ensure adequate voltage surge suppression and protection.
- (4) Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.



Vout(VD	C) Fu	use	Cin®	Cout	TVS
5		15A, slow blow		470µF	SMDJ6.0A
12	14			000	SMDJ14A
15			220µF	220µF	SMDJ17A
24	siow			100. г	SMDJ28A
48				100µF	SMDJ54A

Note:

①Please pay attention to the ambient temperature of the product when using an external capacitor, increase the electrolytic capacitor values to at least 1.5 times the original parameter if the ambient temperature is low(such as  $-25^{\circ}$ C).

#### 2. EMC solution-recommended circuit

We recommended using the recommended circuit shown in Fig.2 during product EMC testing and application.

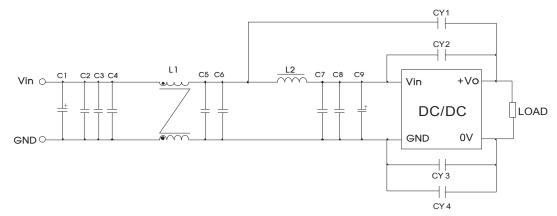
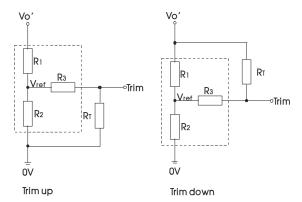


Fig. 2

Components	Recommended Component Value
C1	150µF/100V electrolytic capacitor
С9	47µF/100V electrolytic capacitor
C2、C3、C4、C5、C6、C7、C8	2.2µF/100V ceramic capacitor
Lì	1.0mH/15A common mode inductor
L2	1.5µH/15A inductance
CY1、CY2、CY3、CY4	1nF Y1 safety capacitor

#### 3. Trim Function for Output Voltage Adjustment (open if unused)



TRIM resistor connection (dashed line shows internal resistor network)

Calculation formula of Trim resistance:

up: 
$$R_T = \frac{\alpha R_2}{R_2 - \alpha} - R_3$$
  $\alpha = \frac{Vref}{Vo' - Vref} \cdot R_2$ 

down: RT= 
$$\frac{\alpha R_1}{R_1-\alpha}$$
 -R3  $\alpha = \frac{Vo'-Vref}{Vref} \cdot R_2$ 

Note:

Value for R1, R2, R3, and Vref refer to the above table.

RT: Resistance of Trim.

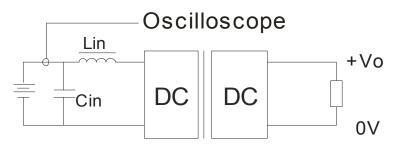
a: User-defined parameter, no actual meanings.

Vo': The trim up/down voltage.

Vout(VDC)	<b>R1(KΩ)</b>	<b>R2(K</b> Ω)	<b>R3(K</b> Ω)	Vref(V)
5	3.036	3	10	2.5
12	11.00	2.87	15	2.5
15	14.03	2.8	15	2.5
24	24.872	2.87	15	2.5
48	53.017	2.913	15	2.5

Note: When using the Trim down function make sure that the RT resistor value is calculated correctly. If the Trim pin is shorted with +Vo, or its value is too low, then the output voltage Vo' would be lower than 0.9Vo, which may cause the product to fail.

#### 4. Reflected ripple current--test circuit



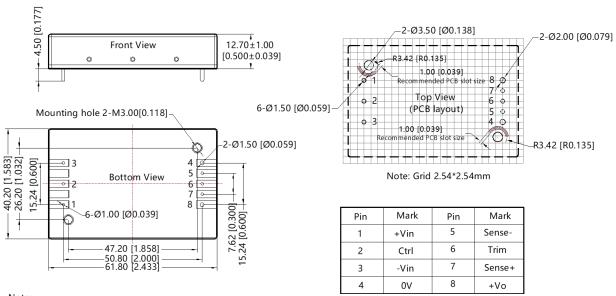
Note:Lin(4.7µH) , Cin(220µF, ESR < 1.0  $\Omega\,$  at 100 KHz)

- 5. The products do not support parallel connection of their output.
- 6. We recommended the use of a converter with higher output power capability to cover applications with higher power requirements.
- 7. For additional information please refer to application notes on www.mornsun-power.com



#### URF48xxQB-150WR3 Dimensions and Recommended Layout



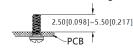


Note: Unit: mm[inch]

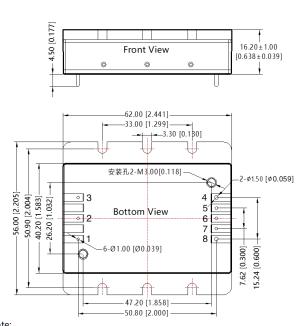
Pin1, 2, 3, 5, 6, 7's diameter: 1.00[0.039] Pin4, 8's diameter: 1.50[0.059] Pin diameter tolerances: ±0.10[±0.004]

General tolerances:  $\pm 0.50[\pm 0.020]$ Mounting hole screwing torque: Max 0.4 N·m

#### Recommended screw length

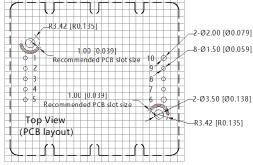


#### URF48xxQB-150WFR3 Dimensions and Recommended Layout



THIRD ANGLE PROJECTION





Note: Grid 2.54\*2.54mm

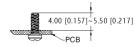
Pin	Mark	Pin	Mark
1	+Vin	5	Sense-
2	Ctrl	6	Trim
3	-Vin	7	Sense+
4	0V	8	+Vo

Unit: mm[inch]

Pin1, 2, 3, 5, 6, 7's diameter: 1.00[0.039]

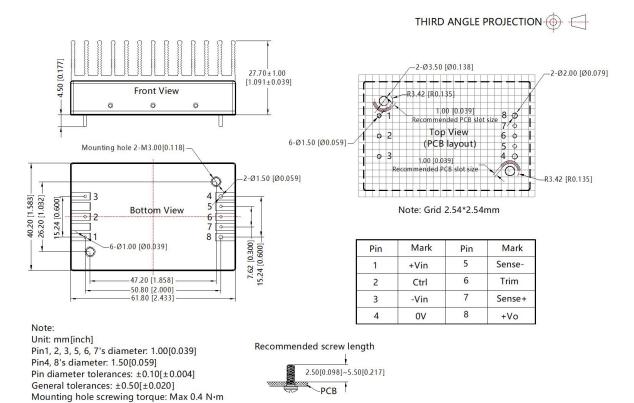
Pin4, 8's diameter: 1.50[0.059] Pin diameter tolerances: ±0.10[±0.004]

General tolerances:  $\pm 0.50[\pm 0.020]$ Mounting hole screwing torque: Max 0.4 N-m Recommended screw length

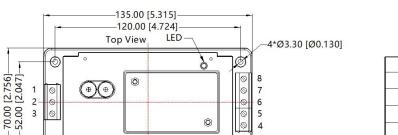


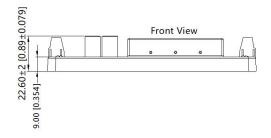


#### URF48xxQB-150WHR3 Dimensions and Recommended Layout



#### URF48xxQB-150WR3A5 Dimensions and Recommended Layout





Pin	Mark
1	+Vin
2	Ctrl
3	-Vin
4	OV
5	Sense-
6	Trim
7	Sense+
8	+Vo

THIRD ANGLE PROJECTION ( )

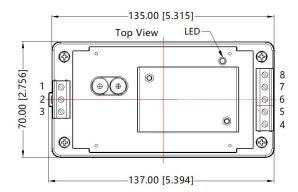
Note: Unit: mm[inch] Wire range: 24-12 AWG Tightening torque: Max 0.4 N·m General tolerances: ±1.00[±0.040]



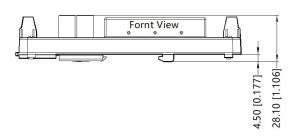
#### URF48xxQB-150WR3A6 Dimensions and Recommended Layout







Pin	Mark
1	+Vin
2	Ctrl
3	-Vin
4	0V
5	Sense-
6	Trim
7	Sense+
8	+Vo

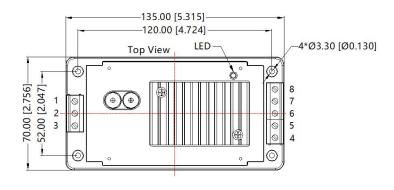


Note: Unit: mm[inch] Wire range: 24-12 AWG Tightening torque: Max 0.4 N·m Installed on DIN RAIL TS35 General tolerances: ±1.00[±0.040]

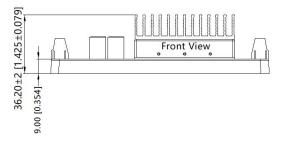
#### URF48xxQB-150WHR3A5 Dimensions and Recommended Layout







Mark
+Vin
Ctrl
-Vin
VO
Sense-
Trim
Sense+
+Vo



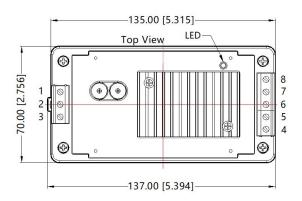
Note: Unit: mm[inch] Wire range: 24-12 AWG Tightening torque: Max 0.4 N·m General tolerances:  $\pm 1.00[\pm 0.040]$ 

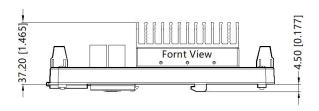


#### URF48xxQB-150WHR3A6 Dimensions and Recommended Layout









Pin	Mark
1	+Vin
2	Ctrl
3	-Vin
4	0V
5	Sense-
6	Trim
7	Sense+
8	+Vo

Note: Unit: mm[inch] Wire range: 24-12 AWG Tightening torque: Max 0.4 N·m

Installed on DIN RAIL TS35 General tolerances: ±1.00[±0.040]

#### Note:

- 1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging bag number: 58010113(URF48xxQB-150WR3), 58200069(URF48xxQB-150WFR3), 58220017(URF48xxQB-150WHR3), 58220031(URF48xxQB-150W(H)R3(A5/A6));
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated load;
- 4. All index testing methods in this datasheet are based on our company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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