1W isolated DC-DC converter
Fixed input voltage, unregulated single output









## **FEATURES**

- Continuous short-circuit protection
- Operating ambient temperature range: -40  $^{\circ}{\rm C}$  to +105  $^{\circ}{\rm C}$
- High efficiency up to 80%
- SMD package
- I/O isolation test voltage 3k VDC
- Low ripple & noise
- Industry standard pin-out

F\_XT-2WR2 series is designed for use in distributed power supply systems and especially suitable in applications such as pure digital circuits, low frequency analog circuits, noise and interference cancelling circuits, relay-driven circuits and data switching circuits, where

- 1. The voltage of the input power supply is relatively stable with a variation of  $\pm 10\%$ Vin or less;
- 2. A high input to output isolation voltage of up to 3000VDC is necessary;
- 3. The requirement for ripple & noise or a tight output regulation is not as strict;
- 4. Typical application areas: Pre-stage interference isolation, ground interference cancellation, pure digital circuits, voltage isolation conversion, general low-frequency analog circuits, relay drive circuits, etc.

		Input Voltage (VDC)	0	output	Full Load	Capacitive	
Certification	n Part No.	Nominal (Range)	Voltage (VDC)	Current (mA) Max./Min.	Efficiency (%) Min./Typ.	Load (µF) Max.	
CE	F0303XT-1WR2		3.3	303/30	65/69		
UL/CE	F0305XT-1WR2	3.3 (2.97-3.63)	5	200/20	70/74		
CE	F0312XT-1WR2	(2177 0100)	12	84/9	72/76		
	F0503XT-1WR2		3.3	303/30	68/72		
	F0505XT-1WR2		5	200/20	76/80	†	
	F0506XT-1WR2	_	6	167/17	76/80		
	F0509XT-1WR2	5 (4.5-5.5)	9	111/12	76/80		
	F0512XT-1WR2	(	12	84/9	76/80		
UL/CE	F0515XT-1WR2		15	67/7	76/80		
OL/CL	F0524XT-1WR2		24	42/4	76/80		
	F1203XT-1WR2		3.3	303/30	68/72	220	
	F1205XT-1WR2		5	200/20	76/80		
	F1209XT-1WR2	12	9	111/12	76/80		
	F1212XT-1WR2	(10.8-13.2)	12	84/9	76/80		
	F1215XT-1WR2		15	67/7	76/80		
	F1224XT-1WR2		24	42/4	76/80		
CE	F1515XT-1WR2	15 (13.5-16.5)	15	67/7	76/80		
	F2405XT-1WR2		5	200/20	76/80		
UL/CE	F2409XT-1WR2	24	9	111/12	76/80		
OL/OL	F2415XT-1WR2	(21.6-26.4)	15	67/7	76/80		
	F2424XT-1WR2		24	42/4	76/80		

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Input Current (full load / no-load)	3.3V input		404/25	/70	mA
	5V input	-	250/20	/60	
	12V input	-	104/15	/50	

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Input Current (full load / no-load)	15V input	-	82/10	/35	A
	24V input	-	52/7	/30	mA
Reflected Ripple Current		-	15		mA
	3.3V input	-0.7		5	VDC
	5V input	-0.7		9	
Surge Voltage (1sec. max.)	12V input	-0.7		18	
	15V input	-0.7		21	
	24V input	-0.7		30	
Input Filter			Capacit	ance filter	
Hot Plug			Unav	ailable	

Item	Operating Conditions		Min.	Тур.	Max.	Unit
Voltage Accuracy			See	output regul	ation curve(Fi	g. 1)
Line ou De ou destina	Input voltage	3.3VDC output			±1.5	
Linear Regulation	change: ±1%	Other outputs	_		±1.2	
		3.3VDC output	-	18		%
	10%-100% load	5VDC output	-	12	_	
		6VDC output	_	10		
Load Regulation		9VDC output		8	-	
		12VDC output	_	7		
		15VDC output		6		
		24VDC output	-	5		
Ripple & Noise*	20MHz bandwidth		-	60	150	mVp-
Temperature Coefficient	Full load				±0.03	%/℃
Short-circuit Protection**	F03xxXT-1WR2/F24	xxXT-1WR2/F0524XT-1WR2			1	s
SHORT-CIRCUIT PROTECTION"	Others		Continuous, self-recovery			

Notes: \* The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information;

<sup>\*\*</sup> At the end of the short circuit duration, the supply voltage must be disconnected from following models: F03xxXT-1WR2 series, F24xxXT-1WR2 series, and F0524XT-1WR2 model.

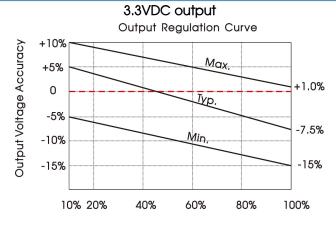
General Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation	Input-output Electric strength test for 1 minute with a leakage current of 1mA max.				VDC
Insulation Resistance	Input-output resistance at 500VDC	1000		-	ΜΩ
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V	-	20	_	pF
Operating Temperature	Derating when operating temperature $\geq$ 100 $^{\circ}$ C, (see Fig. 2)	-40		105	
Storage Temperature		-55		125	rc
Case Temperature Rise	Ta=25 $^{\circ}$ C, nominal input, full load output		25		
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds		300		
Storage Humidity	Non-condensing	-		95	%RH
Reflow Soldering Temperature		time≤60s For actua	ip.≤245°C, s over 217°C al applicatio C J-STD-020	on, please	
Switching Frequency	Full load, nominal input voltage	-	100	-	KHz
MTBF	MIL-HDFK-217F@25°C	3500			K hours

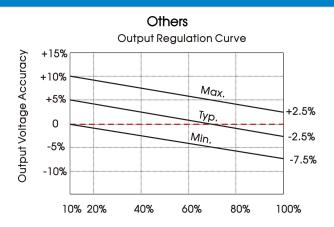


Mechanical Specifications		
Case Material	Black epoxy resin; flame-retardant and heat-resistant (UL94 V-0)	
Dimensions	12.70 x 11.20 x 7.25 mm	
Weight	1.6g(Typ.)	
Cooling Method	Free air convection	

Electromagnetic Compatibility (EMC)				
Francisco -	CE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)		
Emissions	RE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)		
Immunity	ESD	IEC/EN61000-4-2 Contact ±8KV perf. Criteria B		

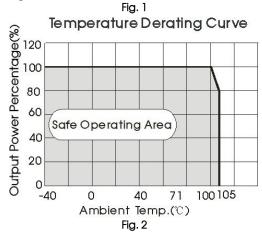
# Typical Characteristic Curves

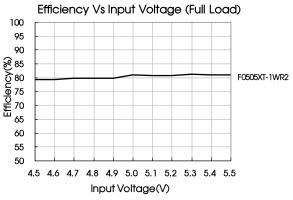


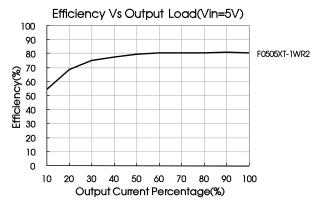


Output Current Percentage (Nominal Input Voltage)

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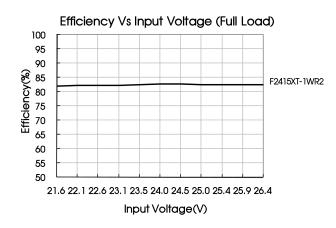


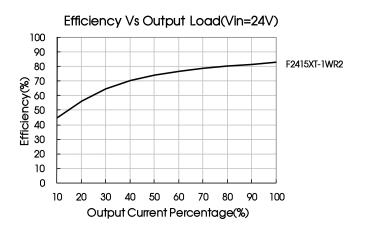




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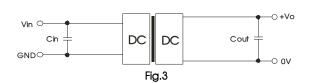


## Design Reference

#### 1. Typical application circuit

Input and/or output ripple can be further reduced, by connecting a filter capacitor from the input and/or output terminals to ground as shown in Fig.3.

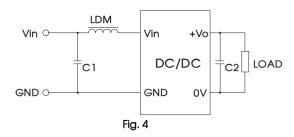
Choosing suitable filter capacitor values is very important for a smooth operation of the modules, particularly to avoid start-up problems caused by capacitor values that are too high. For recommended input and output capacitor values refer to Table 1.



#### Recommended capacitive load value table (Table 1)

Vin(VDC)	Cin(µF)	Vo (VDC)	Cout(µF)
3.3	4.7	3.3	10
5	4.7	5/6	10
12	2.2	9	4.7
15	2.2	12	2.2
24	1	15	1
		24	0.47

#### 2. EMC (CLASS B) compliance circuit



Input voltage (VDC)		3.3/5/12/15/24
Emissions	C1	4.7µF /50V
	C2	Refer to the Cout in Fig.3
	LDM	6.8µH

#### 3. Output load requirements

For a reliable and efficient operation of the converter, the minimum load should never be less than 10% of the rated output load. If the total required output power is below 10%, a parallel bleeding resistor is required on the output, ensuring that the sum of the power consumption is always maintained at 10% minimum.

4. For additional information, please refer to DC-DC converter application notes on www.mornsun-power.com



### **Dimensions and Recommended Layout**

#### THIRD ANGLE PROJECTION

2.10 [0.083]

1.00 [0.039]

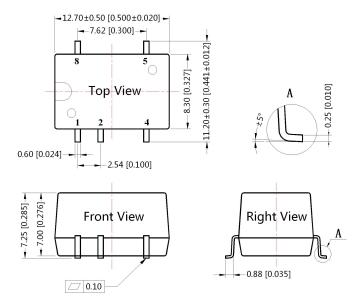
<del>--</del>-7.62 [0.300] -<del>-</del>

Top View





12.20 [0.480]



Note: Grid 2.54\*2.54mm

2.54 [0.100]

Pin-Out		
Pin	Function	
1	GND	
2	Vin	
4	0V	
5	+Vo	
8	NC	

NC: No Connection

Note:

Unit: mm[inch]

Pin section tolerances:  $\pm 0.10[\pm 0.004]$ General tolerances:  $\pm 0.25[\pm 0.010]$ 

#### Notes:

- For additional information on Product Packaging please refer to <u>www.mornsun-power.com</u>. Packaging bag number: 58210024, Roll Packaging bag number: 58200054;
- If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet;
- 3. The maximum capacitive load offered were tested at input voltage range and full load;
- Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 5. All index testing methods in this datasheet are based on our company corporate standards;
- 6. We can provide product customization service, please contact our technicians directly for specific information;
- 7. Products are related to laws and regulations: see "Features" and "EMC";
- 8. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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