

Crystal oscillator

SEIKO EPSON CORPORATION

CRYSTAL OSCILLATOR (SPXO) OUTPUT : CMOS



Product Number (please contact us) SG2016CAN: X1G004801xxxx00 SG-210STF: X1G004171xxxx00 SG3225CAN: X1G005961xxxx15 SG5032CAN: X1G004451xxxx00 SG7050CAN: X1G004481xxxx00

SG2016 / 3225 / 5032 / 7050CAN SG-210STF

- Frequency
- Supply voltage
- Function
- : 1.8 V to 3.3 V Typ. 2
- Standby(<u>s</u>T)

20 standard frequencies

• Operating temperature : -40 °C to +105 °C

:

SG2016CAN SG-210STF



(3.2 x 2.5 mm)





(2.0 x 1.6 mm) (2.5 x 2.0 mm)

SG5032CAN (5.0 x 3.2 mm)

(7.0 x 5.0 mm)

Specifications (characteristics)

Item	Symbol	Specifications					Conditions / Remarks						
	0,	-	MHz	10 MHz	12 MHz				Contain				_
Output frequency	fo		MHz MHz	20 MHz 27 MHz	24 MHz 32 MHz								
			MHz	48 MHz	50 MHz								
			1.6	0 V to 3.63 V	/		4 M⊦	Hz ≤ f	$o \leq 50 \text{ MHz}, \text{T}$	_use = +105 °C	Max.	D ()	
Supply voltage	Vcc	1.71 V to 3.63 V					fo = 7	72 M	Hz, T_use = +	85 °C Max.		Refer to Figure 1	-
		2.25 V to 3.63 V				fo = 72 MHz, T_use = +105 °C Max.							
Storage temperature	T_stg	-55 °C to +125 °C				SG2	016C	AN					
	1_Stg		-40 °C to +125 °C				All ot	All others					
Operating temperature	T_use	-20 °C to +70 °	C, -40	°C to +85 °C	;, -40 °C	to +105 °C	See	of fig	ure *1				
Frequency tolerance	f tol	±25 × 10 ⁻⁶				-20 °	-20 °C to +70 °C						
	1_101		:	±50 × 10 ⁻⁶			-40 °	-40 °C to +85 °C, -40 °C to +105 °C					
		$V_{CC} = 1.8 \text{ V} \pm 10 \text{ \%}$	Vcc :	= 2.5 V ± 10	% Vo	$cc = 3.3 \text{ V} \pm 10 \%$							
		1.5 mA Max.	1	.6 mA Max.		1.8 mA Max.	No lo	No load condition, 4 MHz \leq fo \leq 20 MHz					
Current consumption	Icc	1.8 mA Max.	2	.0 mA Max.		2.2 mA Max.	No lo	No load condition, 20 MHz < fo \leq 40 MHz					
		2.1 mA Max.	2	.4 mA Max.		2.6 mA Max.	No load condition, 40 MHz < fo ≤ 50 MHz						
		2.4 mA Max.	2	.8 mA Max.		3.0 mA Max.	No load condition, fo = 72 MHz						
Stand-by current	I_std	2.1 µA Max.	2.1 μA Max. 2.5 μA Max. 2.7 μA Max.				ST =	=GNE)				
Symmetry	SYM	45 % to 55 % 50 % V_{CC} level, L_CMOS \leq 15 pF											
	Vон		90	% Vcc Min.				он	<u>1.8 V ± 10 %</u> -1.5 mA	2.5 V ± 10 % -3 mA	3.3 V ±		
Output voltage	Vol	10 % V _{CC} Max.						OL	1.5 mA	3 mA	4 r	nA	
Output voltage	V _{OH-2}	V _{cc} - 0.4 V Min.						он	1.8 V±10 % -3 mA	2.5 V±10 % -4 mA	3.3 V±		
	V _{OL-2}	0.4 V Max.						OL	3 mA	4 mA	-01 6 r		
Output load condition (CMOS)	L_CMOS		15 pF Max.										
Input voltage	VIH	80 % Vcc Min.						ST terminal					
input voltage	VIL			$\%$ V_{CC} Max									
Rise time and Fall time	tr / tf	3 ns Max. 3.5 ns Max. (@1.8 V±10 %)				20 % V _{CC} to 80 % V _{CC} level, L_CMOS = 15 pF							
Start-up time	t_str	3 ms Max. T				T = 0	T = 0 at 90 % V _{CC}						
Frequency aging	f_age		±3 × 1	10 ⁻⁶ / year M	ax.		+25 °	°C, F	irst year				

[Model: SG2016/3225/5032/7050CAN]

Product name SG2016 C AN 25.00000MHz T J H A (Standard form) 1 2 3 4567 ①Model ②Output(C: CMOS) ③Frequency ④Supply voltage ⑤Frequency tolerance ⑥Operating temperature range ⑦Internal identification code("A" is default)

@Su	pply voltage *See Figure 1	5Fre	SFrequency tolerance / SOperating temperature range				
T 1.8 V to 3.3 V Typ.		DB*	±25 × 10 ⁻⁶ / -20 °C to +70 °C				
Κ	2.5 V to 3.3 V Typ.	JG	±50 × 10 ⁻⁶ / -40 °C to +85 °C				
		JH	±50 × 10 ⁻⁶ / -40 °C to +105 °C				

* Please refer to Product number list on Full Data Sheet for available frequencies

[Model : SG-210STF]

Т

	1						
Product name	<u>SG-210 S T F 25.000000MHz Y</u>						
(Standard form)	1	23	4	5			
①Model ②Func	tion(S	S:Standby)	③Supply	voltage			
④Frequency ⑤F	=requ	ency tolera	nce				

3.63 105°C 2.25 85°C 1.71 1.60 f_o[MHz] 50 72

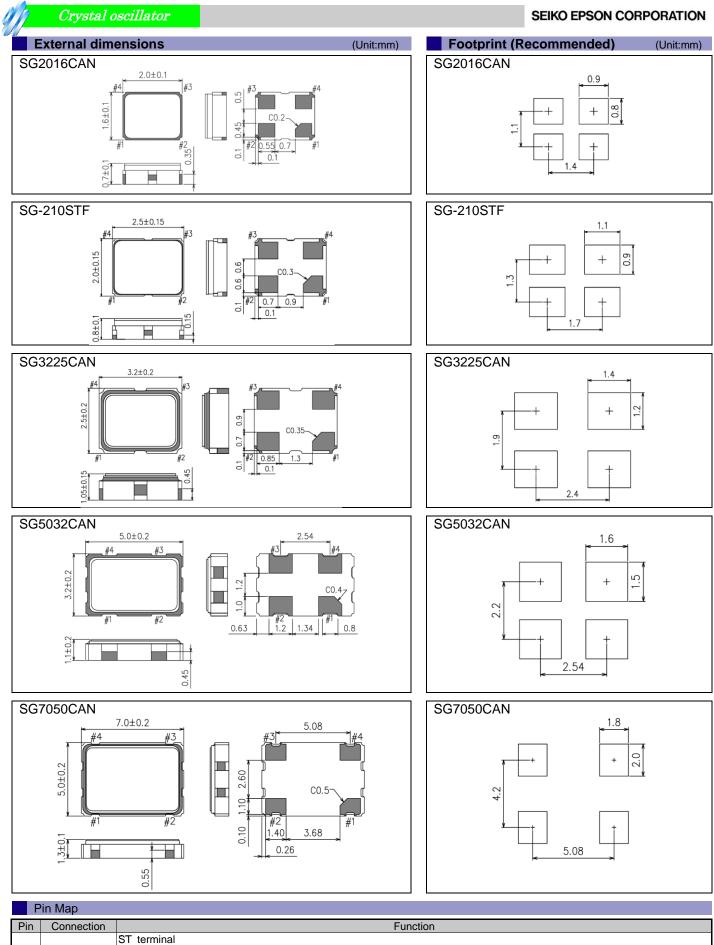
Vcc[V

Figure 1 : The upper limit of Operating temperature and the related conditions

Please note that Supply voltage range (Vcc) depends on Output frequency (fo) and upper limit of Operationg temperature (T_use Max.).

3Supply voltage *See Figure 1 ⑤Frequency tolerance 1.8 V to 3.3 V Typ. S* $\pm 25 \times 10^{-6}$ / -20 °C to +70 °C L $\pm 50 \times 10^{-6}$ / -40 °C to +85 °C ±50 × 10⁻⁶ / -40 °C to +105 °C Y

* Please refer to Product number list on Full Data Sheet for available frequencies



		ST terr	ninal			
1	ST		ST function	Oscillator circuit	Output	
I	31		HIGH or "open"	Oscillation	Specified frequency: Enable	
			LOW	Oscillation stop	High impedance: Disable	
2	GND	Ground				
3	OUT	Clock o	utput			
4	Vcc	Power s	supply			

Notes: To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

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