



SICHUAN HUIYUAN

Huiyuan POF Transceiver Technical Data

5MBd Red light transceiver HY-1521/HY-2521

10MBd Red light transceiver HY-1528/HY-2528

10MBd Green light transceiver HY-1428/HY-2428

(Version V6.0)

Sichuan Huiyuan Plastic Optical Fiber Co., Ltd

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1 Scope

This manual specifies the requirements for the structure, performance, environmental suitability, quality assurance and inspection of the POF transceiver.

2 Product composition

The POF transceiver, including the transmitting device and the receiving device.

The transmitter consists of DATALINK LED chips.

The receiver includes a photodiode, a transimpedance amplifier, an output NPN, and a pull-up resistor, Circuit diagram shown in Figure 1:

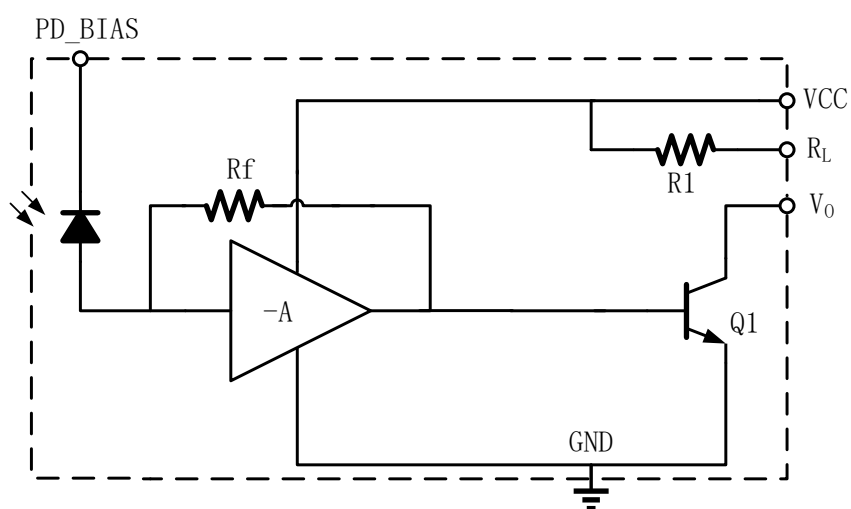


Figure 1 Schematic diagram of the receiver circuit

3 Product shape

3.1 Structure and interface

3.1.1 Dimensions

Huiyuan POF transmitter and receiver have the same size, Its dimensions are shown in Figure 2:

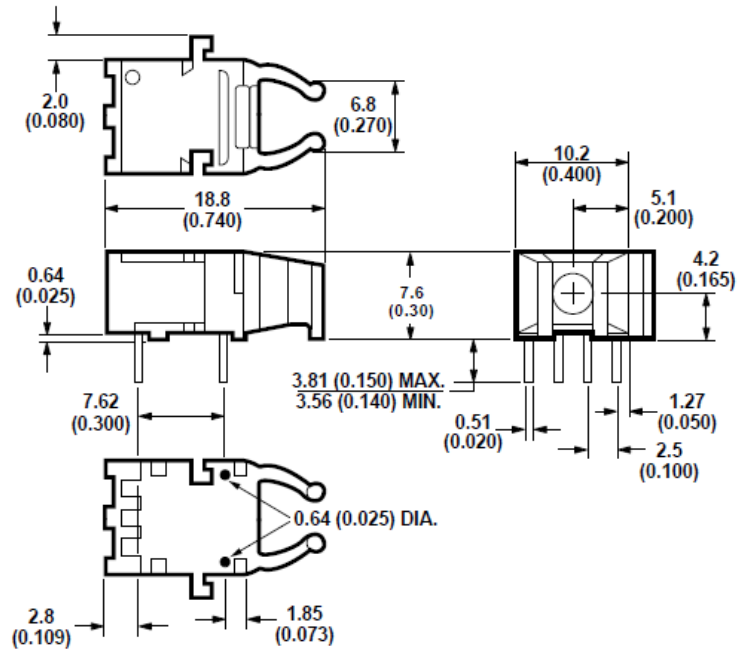


Figure 2 Dimension drawing

3.1.2 Interface definition

- Transmitter interface and pin definition as shown in Figure 3:

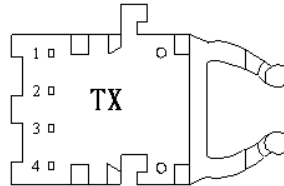


Figure 3 Transmitter interface and pin diagram

Table 1 Description of the transmitter interface

Serial number	Pin name	Pin definition
1	Anode	LED anode
2	Cathode	LED cathode
3	Open	open circuit
4	Open	open circuit

- Receiver device interface and pin definition shown in Figure 4:

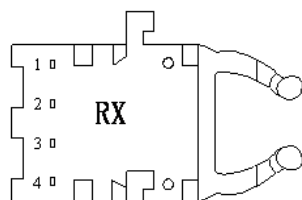


Figure 4 shows the device interface and pin diagram

Table 2 Description of the receiver interface

Serial number	Pin name	Pin definition
1	V_O	TTL output
2	Ground	Chip ground
3	V_{CC}	Chip power supply
4	R_L	Load side

3.1.3 Material requirements

The chip is made of semiconductor material, the chip package is made of transparent epoxy resin material, the device shell is made of RoHS-compliant flame-retardant plastic.

4 5MBd Red light transceiver products HY-1521 / HY-2521 technical parameters

4.1 HY-1521 Transmitter Characteristics

Table 3 Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Reference
Storage Temperature	T_S	-40	+85	$^{\circ}\text{C}$	
Operating Temperature	T_A	-40	+85	$^{\circ}\text{C}$	
Forward DC input current	I_F		80	mA	
Reverse Input voltage	V_R		5	V	$T_a=25^{\circ}\text{C}$
Lead Soldering Cycle			260/10	$^{\circ}\text{C/s}$	

Table 4 Electrical/Optical Characteristics

Operating temperature range: -40°C to 85°C , Power supply voltage range: $4.75\text{V} < V_{\text{CC}} < 5.25\text{V}$

Parameter	Symbol	Min.	Typ.	Max.	Units	Reference
Transmitter output optical power	P_{T}	-7.5	-6	-5	dBm	$I_{\text{F}}=60\text{mA}$, Remarks 1
Output optical power temperature coefficient	$\Delta P_{\text{T}}/\Delta T$		-0.02		%/ $^{\circ}\text{C}$	
Peak emission wavelength	λ_{PK}		650		nm	
Forward Voltage	V_{F}	1.85	2	2.4	V	$I_{\text{F}}=60\text{mA}$
Effective Diameter	D		1.1		mm	
Reverse Input Breakdown Voltage	V_{BR}	5.0	11.0		V	$I_{\text{F}}=10\text{ uA}$, $T_{\text{A}}=25^{\circ}\text{C}$
Numerical Aperture	NA		0.5			

Remarks 1: Measured at the end of the fiber optic cable with large area detector.the length of the standard fiber is 0.5m.

4.2 HY-2521 Receiver Characteristics

Table 3 Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Reference
Storage Temperature	T_{S}	-40	+85	$^{\circ}\text{C}$	
Operating Temperature	T_{A}	-40	+85	$^{\circ}\text{C}$	
Lead Soldering Cycle			260/10	$^{\circ}\text{C}/\text{s}$	Remarks 1
Supply Voltage	V_{CC}	-0.5	6	V	Remarks 2
Output Collector Current	I_{OAV}		8	mA	
Output Collector Power Dissipation	P_{OD}		40	mW	
Output Voltage	V_{O}	0	6	V	
Pull-up Voltage	V_{P}	-5	V_{CC}	V	
Fan Out (TTL)	N		5		

Remarks 1: 1.6 mm below seating plane.

Remarks 2: When applying this receiver chip, a $0.1\text{ }\mu\text{F}$ filter capacitor is required between the VCC and GND pins. The filter capacitor is as close as possible to the PIN pin of GND and Vcc.

Table 6 Electrical/Optical Characteristics

Temperature range: -40°C to 85°C , Power supply voltage range: $4.75\text{ V} < V_{\text{CC}} < 5.25\text{ V}$

Parameter	Symbol	Min.	Typ.	Max.	Units	Reference
Peak wavelength	λ		650		nm	
Data rate	S_{r}	DC		5	MBd	$\text{BER} \leq 10^{-9}$, PRBS:2 ⁷ -1
Input optical power Level for Logic "0"	P_{RL}	-17.5		-2.5	dBm	$V_{\text{OL}}=0.5\text{V}$, $I_{\text{OL}}=8\text{mA}$, remarks 1
Input optical power Level for Logic "1"	P_{RH}			-20	dBm	$V_{\text{OL}}=5\text{V}$, $I_{\text{OH}} \leq 250\text{ }\mu\text{A}$
High level output current	I_{OH}		5	250	μA	$V_{\text{O}}=5\text{V}$, $P_{\text{R}}=0$, R_{L} open
Low level output current	V_{OL}		0.4	0.5	V	$I_{\text{OL}}=8\text{mA}$, $P_{\text{R}}=P_{\text{RLMIN}}$, R_{L} open
High level supply current	I_{CCH}		4.2	5	mA	$V_{\text{CC}}=5\text{V}$, $P_{\text{R}}=0$, R_{L} open
Low level supply current	I_{CCL}		4.4	5	mA	$V_{\text{CC}}=5\text{V}$, $P_{\text{R}}=-11.5\text{ dBm}$, R_{L} open
Internal pull-up resistor	R_{L}	800	1000	1200	Ω	
Effective diameter	D		1.0		mm	
Numerical Aperture	NA		0.5			

Remarks 1: The dynamic range here is measured based on the duty cycle of the received waveform (50 ± 17%).

4.3 HY-1521/HY-2521 System Link Performance

Table 7 System Performance

Temperature range: -40°C to 85°C

Parameter	Symbol	Min.	Typ.	Max.	Units	Reference
Data rate	Sr	DC		5	MBd	BER≤10 ⁻⁹ , PRBS:2 ⁷ -1
Link Distance	L		20	40	m	I _{Fdc} =60mA, Refer to Figure 5, Fiber selection 650 attenuation 0.2dB / m,remarks 1
Propagation Delay(low to high)	t _{PLH}		68		ns	The fiber length is 0.5 m,P _R = -11dBm
Propagation Delay(high to low)	t _{PHL}		46		ns	
Pulse width Distortion t _{PLH} -t _{PHL}	Δt _w		22		ns	P _R = -11dBm

Remarks 1: The link distance here is measured based on the duty cycle of the received waveform (50 ± 17%).

5 10MBd Red device products HY-1528/HY-2528 technical parameters

5.1 HY-1528 Transmitter Characteristics

Table 8 Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Reference
Storage Temperature	T _s	-40	+85	°C	
Operating Temperature	T _A	-40	+85	°C	
Forward DC input current	I _F		80	mA	
Reverse Input voltage	V _R		5	V	T _a =25°C
Lead Soldering Cycle			260/10	°C/s	

Table 9 Electrical/Optical Characteristics

Operating temperature range: -40°C to 85°C, Power supply voltage range: 4.75V<V_{CC}<5.25V

Parameter	Symbol	Min.	Typ.	Max.	Units	Reference
Transmitter output optical power	P _T	-5.0	-3	-1	dBm	I _F =60mA, remarks 2
Output optical power temperature coefficient	ΔP _T /ΔT		-0.02		%/°C	
Peak emission wavelength	λ _{PK}		650		nm	
Forward Voltage	V _F	1.8	2	2.4	V	I _F =60mA
Effective Diameter	D		1.1		mm	
Reverse Input Breakdown Voltage	V _{BR}	5.0	11		V	I _F = 10 uA, T _A = 25°C
Numerical Aperture	NA		0.5			

Remarks 1: With a large area of the detector after 0.5m standard fiber after the measurement.

5.2 HY-2528 Receiver Characteristics

Table 10 Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Reference
Storage Temperature	T _s	-40	+85	°C	
Operating Temperature	T _A	-40	+85	°C	
Lead Soldering Cycle			260/10	°C/s	Remarks 1
Supply Voltage	V _{CC}	-0.5	6	V	Remarks 2
Output Collector Current	I _{OAV}		8	mA	
Output Collector Power Dissipation	P _{OD}		40	mW	
Output Voltage	V _O	0	6	V	
Pull-up Voltage	V _P	-5	V _{CC}	V	
Fan Out (TTL)	N		5		

Remarks 1: 1.6 mm below seating plane.

Remarks 2: When applying this receiver chip, a 0.1 μ F filter capacitor is required between the VCC and GND pins. The filter capacitor is as close as possible to the PIN pin of GND and Vcc.

Table 11 Electrical/Optical Characteristics

Temperature range: -40°C to 85°C, Power supply voltage range: 4.75 V < V_{CC} < 5.25 V

Parameter	Symbol	Min.	Typ.	Max.	Units	Reference
Peak wavelength	λ		650		nm	
Data rate	Sr	DC		10	MBd	BER $\leq 10^{-9}$, PRBS:2 ⁷ -1
Input optical power Level for Logic "0"	P _{RL}	-26		-1	dBm	V _{OL} =0.5V, I _{OL} =8mA, remarks 1
Input optical power Level for Logic "1"	P _{RH}			-33	dBm	V _{OL} =5V, I _{OH} $\leq 250\mu$ A
High level output current	I _{OH}		5	250	μ A	V _O =5V, P _R =0, R _L open
Low level output current	V _{OL}	-0.5	0.4	0.5	V	I _{OL} =8mA, P _R =P _{RLMIN} , R _L open
High level supply current	I _{CCH}		4.2	5	mA	V _{CC} =5V, P _R =0, R _L open
Low level supply current	I _{CCL}		4.4	5	mA	V _{CC} =5V, P _R =-11.5 dBm, R _L open
Internal pull-up resistor	R _L	700	1000	1500	Ω	
Effective diameter	D		1.0		mm	
Numerical Aperture	NA		0.5			

Remarks 1: The dynamic range here is measured based on the duty cycle of the received waveform (50 $\pm$ 17%).

5.3 HY-1528/HY-2528 System Link Performance

Table 12 System Characteristics

Temperature range: -40°C to 85°C

Parameter	Sym bol	Min.	Typ.	Max.	Units	Reference
Data rate	Sr	DC		10	MBd	$BER \leq 10^{-9}$, PRBS:2 ⁷ -1
Link Distance	L		80	100	m	$I_{Fdc}=60mA$, Refer to Figure 5, Fiber selection 650 attenuation 0.2dB/m,remarks 1
Propagation Delay(low to high)	t_{PLH}		50		ns	The fiber length is 0.5 m $P_R = -15dBm$
Propagation Delay(high to low)	t_{PHL}		40		ns	
Pulse width Distortion $t_{PLH}-t_{PHL}$	Δt_w		10		ns	$P_R = -15dBm$

Remarks 1: The link distance here is measured based on the duty cycle of the received waveform ($50 \pm 17\%$).

6 10MBd Green device products HY-1428/HY-2428 technical parameters

6.1 HY-1428 Transmitter Characteristics

Table 13 Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Reference
Storage Temperature	T_s	-40	+85	°C	
Operating Temperature	T_A	-40	+85	°C	
Forward DC input current	I_F		80	mA	
Reverse Input voltage	V_R		5	V	$T_A=25^\circ C$
Lead Soldering Cycle			260/10	°C/s	

Table 14 Electrical/Optical Performance

Operating temperature range: -40°C to 85°C, Power supply voltage range: $4.75V < V_{CC} < 5.25V$

Parameter	Symbol	Min.	Typ.	Max.	Units	Reference
Transmitter output optical power	P_T	-3	-1	-0.5	dBm	$I_F=60mA$, remarks 1
Output optical power temperature coefficient	$\Delta P_T / \Delta T$		-0.1		%/°C	
Peak emission wavelength	λ_{PK}		520		nm	
Forward Voltage	V_F	3.2	3.4	3.8	V	$I_F=60mA$
Effective Diameter	D		1.1		mm	
Reverse Input Breakdown Voltage	V_{BR}	5.0	11		V	$I_F=10\mu A$, $T_A=25^\circ C$
Numerical Aperture	NA		0.5			

Remarks 1: With a large area of the detector after 0.5m standard fiber after the measurement.

6.2 HY-2428 Receiver Characteristics

Table 15 Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Reference
Storage Temperature	T _s	-40	+85	°C	
Operating Temperature	T _A	-40	+85	°C	
Lead Soldering Cycle			260/10	°C/s	Remarks 1
Supply Voltage	V _{CC}	-0.5	6	V	Remarks 2
Output Collector Current	I _{OAV}		8	mA	
Output Collector Power Dissipation	P _{OD}		40	mW	
Output Voltage	V _O	0	6	V	
Pull-up Voltage	V _P	-5	V _{CC}	V	
Fan Out (TTL)	N		5		

Remarks 1: 1.6 mm below seating plane.

Remarks 2: When applying this receiver chip, a 0.1 μ F filter capacitor is required between the VCC and GND pins. The filter capacitor is as close as possible to the PIN pin of GND and Vcc.

Table 16 Electrical/Optical Characteristics

Temperature range: -40°C to 85°C, Power supply voltage range: 4.75 V < V_{CC} < 5.25 V

Parameter	Symbol	Min.	Typical value	Max.	Units	Reference
Peak wavelength	λ		520		nm	
Data rate	Sr			10	MBd	BER $\leq 10^{-9}$, PRBS:2 ⁷ -1
Input optical power Level for Logic “0”	P _{RL}	-26		0	dBm	V _{OL} =0.5V, I _{OL} =8mA, remarks 1
Input optical power Level for Logic “1”	P _{RH}			-33	dBm	V _{OL} =5V, I _{OH} $\leq 250\mu$ A
High level output current	I _{OH}		5	250	μ A	V _O =5V, P _R =0, R _L open
Low level output current	V _{OL}	-0.5	0.4	0.5	V	I _{OL} =8mA, P _R =P _{RLMIN} , R _L open
High level supply current	I _{CCH}		4.2	5	mA	V _{CC} =5V, P _R =0, R _L open
Low level supply current	I _{CCL}		4.4	5	mA	V _{CC} =5V, P _R =-11.5 dBm, R _L open
Internal pull-up resistor	R _L	700	1000	1500	Ω	
Effective diameter	D		1.0		mm	
Numerical Aperture	NA		0.5			

Remarks 1: The link distance here is measured based on the duty cycle of the received waveform (50 $\pm$ 17%).

6.3 HY-1428/HY-2428 System Link Performance

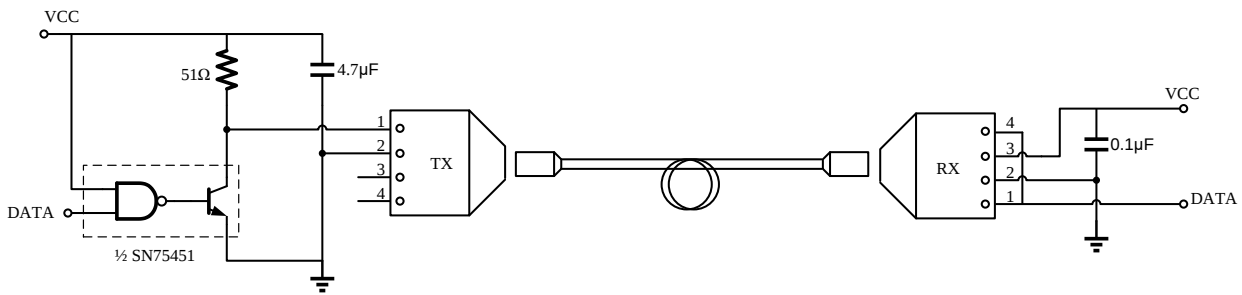
Table 17 System Characteristics

Temperature range: -40°C to 85°C

Parameter	Symbol	Min.	Typical value	Max.	Units	Reference
Data rate	Sr	DC		10	MBd	BER≤10 ⁻⁹ , PRBS:2 ⁷ -1
Link Distance	L		200	240	m	IFdc=60mA, Refer to Figure 5 , Fiber selection 520 attenuation0.1dB/ m,remarks 1
Propagation Delay(low to high)	t _{PLH}		66		ns	The fiber length is 0.5 m P _R = -11dBm
Propagation Delay(high to low)	t _{PHL}		46		ns	
Pulse width Distortion t _{PLH} -t _{PHL}	Δt _w		20		ns	P _R = -11dBm

Remarks 1: The link distance here is measured based on the duty cycle of the received waveform (50 ± 17%).

7 Typical application of optical transceiver devices



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Figure 5 system application circuit diagram

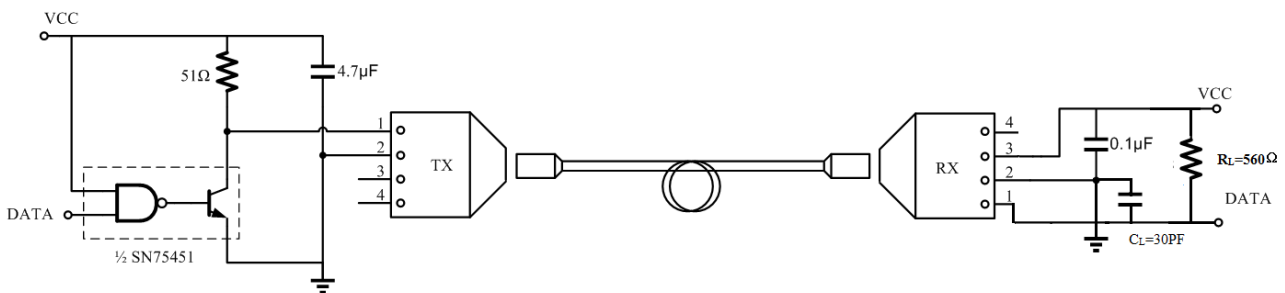


Figure6 . 5MBd propagation delay test circuit

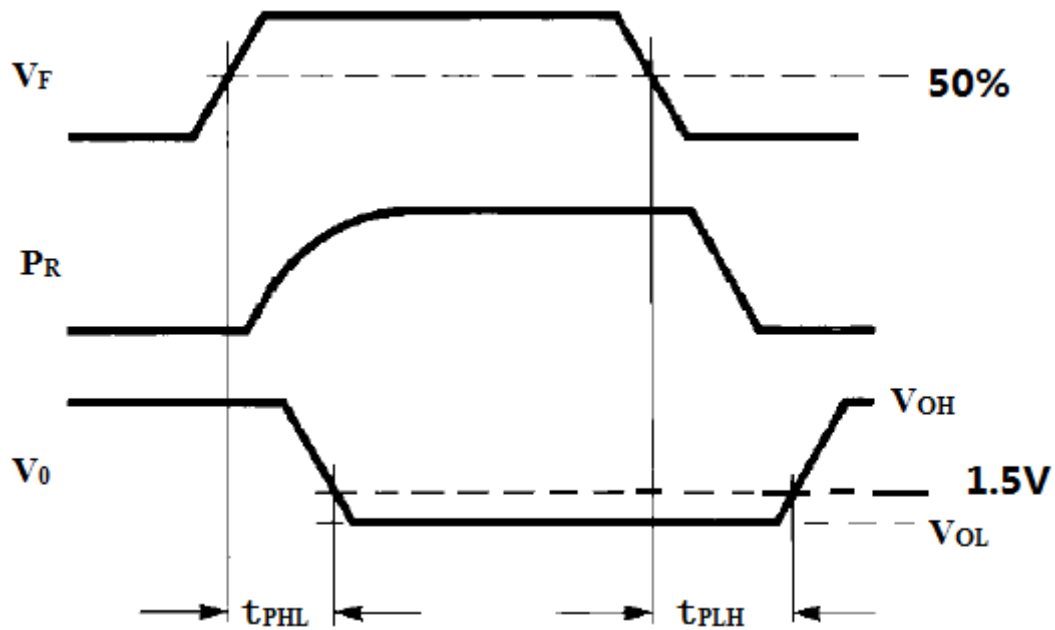


Figure7 Propagation delay test waveforms

8 Other

Product full range of no self-excited, and when the power ripple within 100mV performance to meet the requirements of parameters.

9 Storage

The product shall be packed in the corresponding packaging container provided by the purchaser. The packaging container should be stored in a drying oven or drying tower with a relative humidity of not more than 30% and maintained at a temperature range of 10 °C ~ 30 °C. The chip satisfying the above conditions is valid Storage period is more than 4 years.