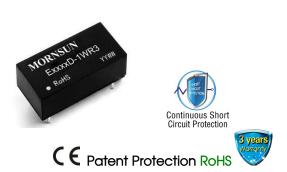


1W isolated DC-DC converter,

Fixed input voltage and unregulated dual output



FEATURES

- Continuous short-circuit protection
- No-load input current as low as 5mA
- Operating ambient temperature range: -40℃ to +105℃
- High efficiency up to 84%
- DIP package
- I/O Isolation test voltage: 3k VDC
- Industry standard pin-out
- Designed to meet UL62368 safety standards
- EN62368 Approval

E05_D-1WR3 series are specially designed for applications where isolation is required between dual output and input in a distributed power supply system. They are suitable for: pure digital circuits, low frequency analog circuits, relay-driven circuits and data switching circuits, etc.

Selection (Guide					
		Input Voltage (VDC)	VDC) Output Voltage (VDC) Current(mA) Max./Min.			
Certification	Part No.	Nominal (Range)			Full Load Efficiency (%) Min./Typ.	Max. Capacitive* Load(µF)
	E0505D-1WR3	5 (4.5-5.5)	±5	±100/±10	78/82	1200
CL.	E0509D-1WR3		±9	±56/±6	80/84	470
CE	E0512D-1WR3		±12	±42/± 5	80/84	220
	E0515D-1WR3		±15	±34/±3	80/84	220

Note:* The capacitive load for positive and negative outputs is identical.

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
	5VDC output		244/5	256/10	mA
Input Current (full load / no-load)	9VDC/12VDC output		238/10	250/20	
	15VDC output		238/18	250/30	
Reflected Ripple Current*			15		
Input Filter			Capacit	ance Filter	
Hot Plug		Unavailable			

Note: * Please refer to DC-DC Converter Application Note for detailed description of Reflected ripple current testing method.

Item	Operating Conditi	ons	Min.	Typ.	Max.	Unit
Voltage Accuracy				See Output Regulation Curve (Fig. 1)		
Linear Regulation	Input voltage cha	nge: ±1%			1.2	
	10%-100% load	5VDC output		10	15	%
Lead Demilation		9VDC output		8	10	
Load Regulation		12VDC output		7	10	
		15VDC output		6	10	
Ripple & Noise *	20MHz bandwidth	20MHz bandwidth		30	75	mVp-p
Temperature Coefficient	Full load	Full load		±0.02		%/ ℃
Short-circuit Protection				Continuous	self-recovery	,

Note:* The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information.



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DC/DC Converter E05_D-1WR3 Series

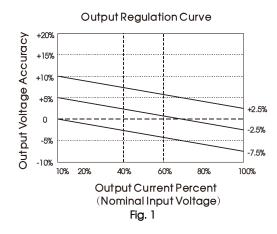
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General Specificati	ons				
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation	Input-output Electric Strength Test for 1 minute with a leakage current of 1mA max.	3000			VDC
Insulation Resistance	Input-output resistance at 500VDC	1000			MΩ
Isolation Capacitance	Input-output capacitance at 100KHz/0.1V		20		pF
Operating Temperature	Derating if the temperature \geq 85°C, (see Fig. 2)	-40		105	ĉ
Storage Temperature		-55		125	C
Case Temperature Rise	Tα=25℃		15		
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			300	Ĉ
Storage Humidity	Non-condensing			95	%RH
Vibration		10-150Hz	, 0.75mm, 5g,	90min, along	y X, Y and Z
Switching Frequency	Full load, nominal input voltage		260		KHz
MTBF	MIL-HDBK-217F@25°C	3500			K hours

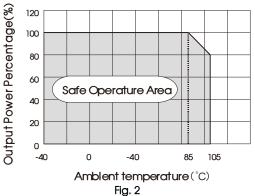
Mechanical Specifications			
Case Material	Black plastic; flame-retardant and heat-resistant (UL94 V-0)		
Dimensions	20.00 x 10.00 x 7.00mm		
Weight	2.4g(Тур.)		
Cooling Method	Free air convection		

Electromagnetic compatibility (EMC)				
Emissions	CE	ISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)		
	RE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)		
Immunity	ESD	IEC/EN61000-4-2 Air ±8kV, Contact ±4kV perf. Criteria B		

Typical Characteristic Curves



Temperature Derating Curve



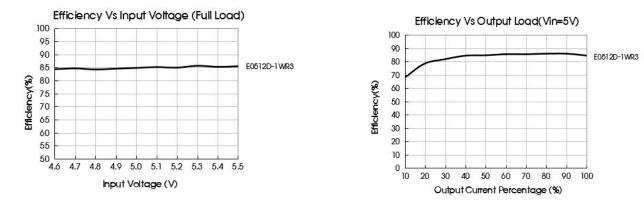
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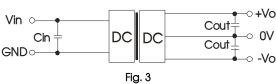


Design Reference

1. Typical application circuit

Input and/or output ripple can be further reduced by connecting capacitor filters to the input and/or output terminals of the DC-DC converter as shown in Figure 3. Also, the capacitance of the output filter capacitor must be properly selected. If the capacitor value that is too high, the converter may not be able to properly start up. To ensured safe and reliable operation, the specified filter capacitor value in Table 1 must not be exceeded.

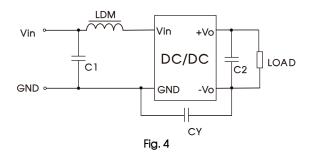
Dual



lable I Red	commended	capacitive	load vo	alue table

Vin(VDC)	Cin(µF)	Vo (VDC)	Cout(µF)
		5	4.7
5	4.7	9/12	1
		15	0.47

2. EMC compliance circuit



Input voltage 5VDC	0	(VDC)	5/9	12/15
		C1、C2	4.7µF /50V	4.7µF /50∨
	EMI	LDM	6.8µH	6.8µH
		CY		1nF/3KV

3. Output load requirements

For a reliable and efficient operation of the converter, the minimum load should never be below 10% of the rated output load. If the total required output power is less than 10%, a parallel bleeding resistor is required, ensuring that the sum of the power consumption is always maintained at 10% minimum.

4. For additional information please refer to DC-DC converter application notes on www.mornsun-power.com



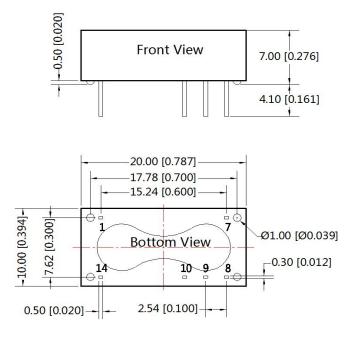
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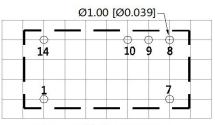
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Dimensions and Recommended Layout

THIRD ANGLE PROJECTION

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Note : Grid 2.54*2.54mm

Pin-Out				
Pin	Dual			
1	GND			
7	NC			
8	+Vo			
9	0V			
10	-Vo			
14	Vin			

NC:No connection

Note: Unit:mm[inch] Pin section tolerances:±0.10[±0.004] General tolerances:±0.25[±0.010]

Notes:

- 1. For additional information on Product Packaging please refer to www.mornsun-power.com. The Packaging bag number of Horizontal packaging:58200009;
- 2. If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet;
- 3. The maximum capacitive load offered were tested at input voltage range and full load;
- 4. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 5. All index testing methods in this datasheet are based on our company corporate standards;
- 6. We can provide product customization service, please contact our technicians directly for specific information;
- 7. Products are related to laws and regulations: see "Features" and "EMC";
- 8. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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