

# DRD5240W34

## Rectifier Diode



DS6013 – 1 March 2011 (LN28200)

### FEATURES

- Double Side Cooling
- High Surge Capability

### KEY PARAMETERS

$V_{RRM}$	<b>3400V</b>
$I_{F(AV)}$	<b>5240A</b>
$I_{FSM}$	<b>64200A</b>

### VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages $V_{RRM}$ V	Conditions
DRD5240W34	3400	$V_{RSM} = V_{RRM} + 100V$
DRD5240W32	3200	
DRD5240W30	3000	
DRD5240W28	2800	
DRD5240W26	2600	
DRD5240W24	2400	

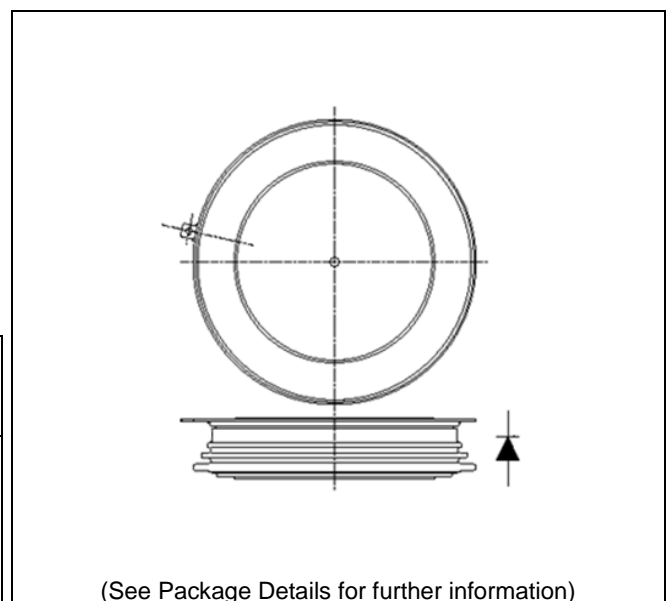


Fig. 1 Package outline

### ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

**DRD5240W34** for a 3400V device

## CURRENT RATINGS

$T_{case} = 75^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
<b>Double Side Cooled</b>				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	6280	A
$I_{F(RMS)}$	RMS value	-	9860	A
$I_F$	Continuous (direct) on-state current	-	8880	A

$T_{case} = 100^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
<b>Double Side Cooled</b>				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	5240	A
$I_{F(RMS)}$	RMS value	-	8230	A
$I_F$	Continuous (direct) on-state current	-	7410	A

## SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
$I_{FSM}$	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 175^{\circ}\text{C}$	64.2	kA
$I^2t$	$I^2t$ for fusing	$V_R = 0$	20.61	$\text{MA}^2\text{s}$

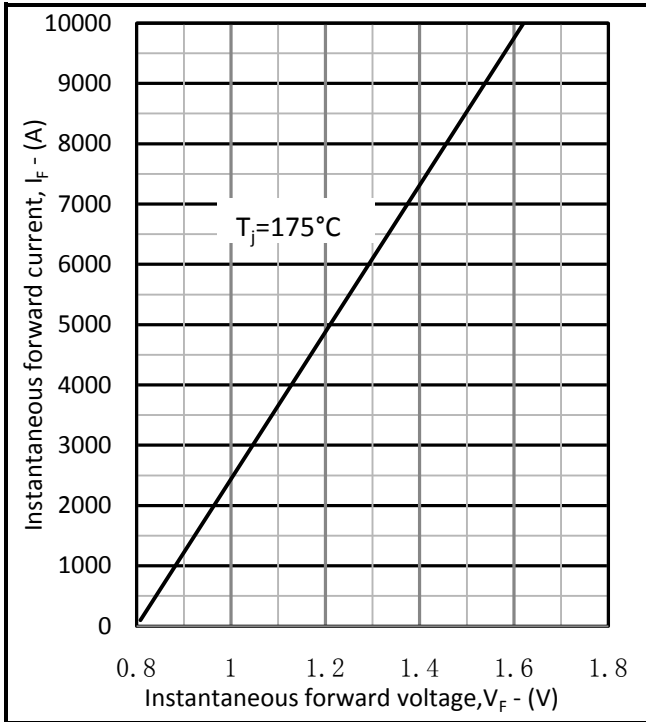
**THERMAL AND MECHANICAL RATINGS**

Symbol	Parameter	Test Conditions		Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance – junction to case	Double side cooled	DC	-	0.007	°C/W
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.002	°C/W
$T_{vj}$	Virtual junction temperature	Blocking $V_{DRM} / V_{RRM}$		-40	175	°C
$T_{stg}$	Storage temperature range			-40	175	°C
$F_m$	Clamping force			62	78	kN

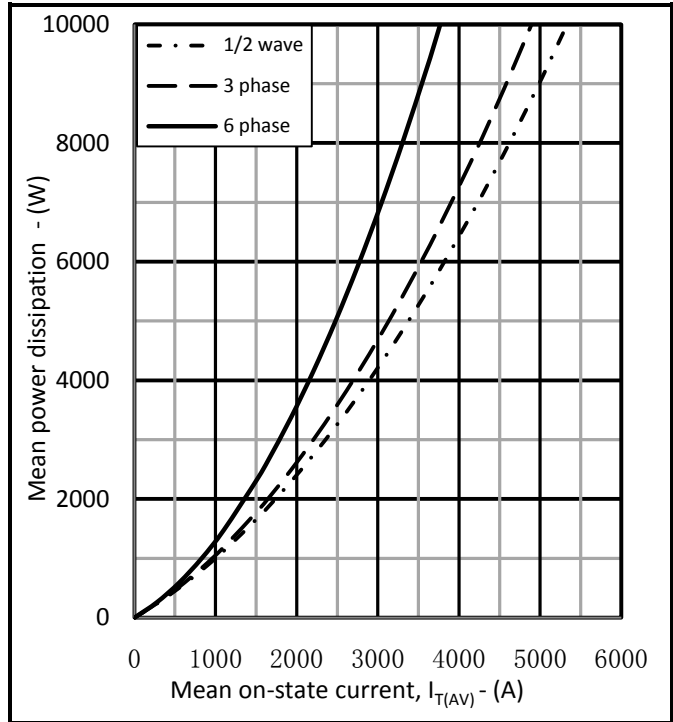
**CHARACTERISTICS**

Symbol	Parameter	Test Conditions	Min.	Max.	Units
$V_{FM}$	Forward voltage	At 6000A peak, $T_{case} = 175^{\circ}C$	-	1.29	V
$I_{RM}$	Peak reverse current	At $V_{DRM}$ , $T_{case} = 175^{\circ}C$	-	400	mA
$Q_S$	Total stored charge	$I_F = 4000A$ , $di_{RR}/dt = 10A/\mu s$ $T_{case} = 175^{\circ}C$ , $V_R = 100V$	-	6000	$\mu C$
$V_{TO}$	Threshold voltage	At $T_{vj} = 175^{\circ}C$	-	0.80	V
$r_T$	Slope resistance	At $T_{vj} = 175^{\circ}C$	-	0.082	$m\Omega$

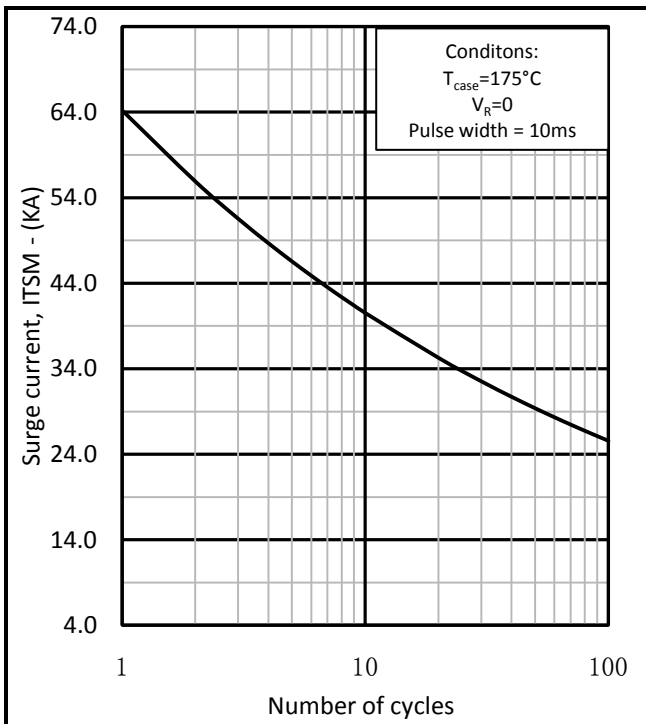
**CURVES**



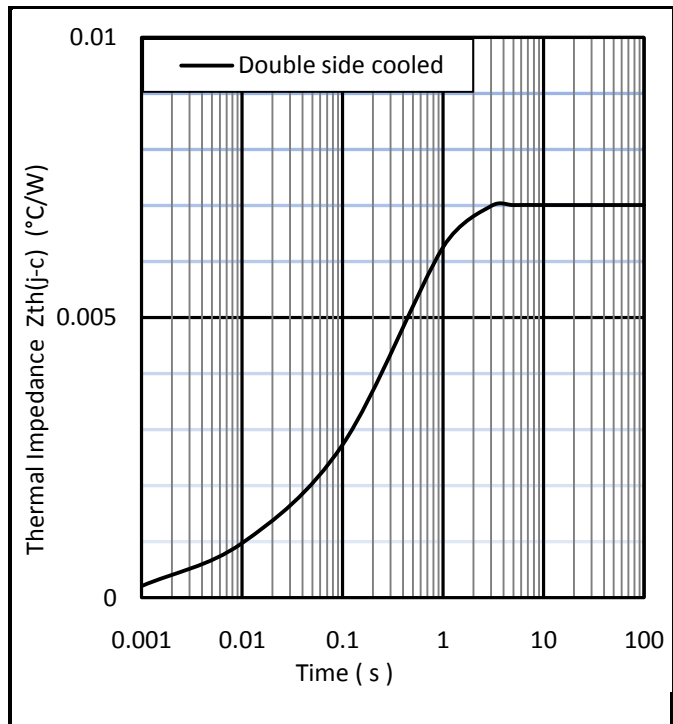
**Fig.2 Maximum forward characteristics**



**Fig.3 Dissipation curves**



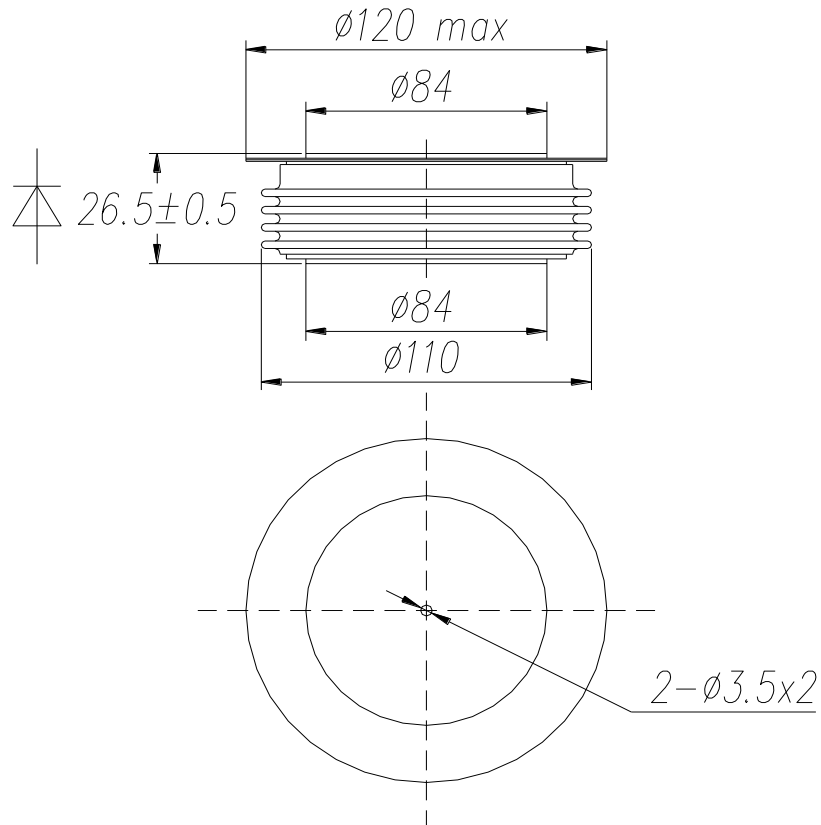
**Fig.4 Surge (Non-Repetitive) Forward current vs time**



**Fig.5 Maximum (limit) transient thermal impedance-junction to case**

**PACKAGE DETAILS**

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



**Package outline type code: W**

**Note:**

Some packages may be supplied with gate and or tags.

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